

PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

**Complete if Known**

Application Number	10/776,475
Filing Date	2/10/2004
First Named Inventor	Gunn III, et al
Art Unit	Not Yet Assigned
Examiner Name	
Attorney Docket Number	LUX - P019

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
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**FOREIGN PATENT DOCUMENTS**

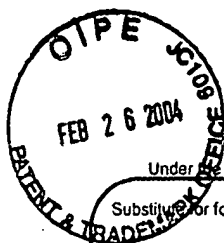
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>8</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

Examiner Signature	<i>Daniel Pithon</i>	Date Considered	11/16/05
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Art Unit	Not Yet Assigned		
Examiner Name	"		
Attorney Docket Number	LUX - P019		
Sheet	2	of	2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DGP	1	"LOW LOSS NANOPHOTONIC WAVEGUIDES AND RING RESONATORS IN SILICON-ON-INSULATOR"; BEATS, ET AL	
DGP	2	"LARGE-SCALE PRODUCTION TECHNIQUES FOR PHOTONIC NANOSTRUCTURES"; BOGAERTS, ET AL	
DGP	3	"CMOS-COMPATIBLE PLANAR SILICON WAVEGUIDE-GRATING-COUPLER PHOTODETECTORS FABRICATED ON SILICON-ON-INSULATOR (SOI) SUBSTRATES"; S.M. CSUTAK, S. DAKSHINA-MURTHY &	
DGP		JOE C. CAMPBELL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL.38 NO. 5 MAY 2002	
DGP	4.	"BURIED-OXIDE SILICON-ON-INSULATOR STRUCTURES II: WAVEGUIDE GRATING COUPLERS"; EMMONS, ET AL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL.28 NO. 1 JANUARY 1992.	
DGP	5	"INTEGRATED OPTICS FOR OPTICAL HEAD APPLICATIONS"; OCHIAI ET AL; ELECTRONICS LETTERS 18TH AUGUST 1994 VOL. 30 NO. 17, PAGE 1403-1404	
DGP	6	"EFFECTS OF GRATING HEIGHTS ON HIGHLY EFFICIENT UNIBOND SOI WAVEGUIDE GRATING COUPLERS"; ANG, ET AL; IEEE PHOTONICS TECHNOLOGY LETTERS VOL.12 NO. 1 JAN 2000	
DGP	7	"MODE MATCHING TECHNIQUE FOR HIGHLY EFFICIENT COUPLING BETWEEN DIELECTRIC WAVEGUIDES AND PLANAR PHOTONIC CRYSTAL CIRCUITS"; SANCHIS, ET AL	
DGP	8	"BURIED-OXIDE SILICON-ON-INSULATOR STRUCTURES II: WAVEGUIDE GRATING COUPLERS"; EMMONS, ET AL; IEEE JOURNAL OF QUANTUM ELECTRONICS, VOL 28 NO.1 JANUARY 1992	
DGP	9	"NON UNIFORM GRATING COUPLERS FOR COUPLING OF GAUSSIAN BEAMS TO COMPACT WAVEGUIDES"; WEST, ET AL; PUBLISHED IN THE INTEGRATED PHOTONICS RESEARCH TECHNICAL DIGEST 1994	

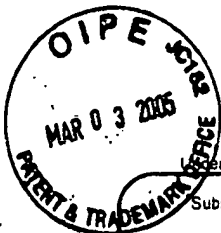
Examiner Signature	Daniel Pithers	Date Considered	11/16/05
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Art Unit	2817
Examiner Name	(not yet assigned)
Attorney Docket Number	LUX-P019

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**U. S. PATENT DOCUMENTS**

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		Number-Kind Code <sup>2</sup> (if known)			
	1	US- 6,285,813	09-04-2001	Schultz et al.	385/37
	2	US- 5,101,459	03-31-1992	Sunagawa	385/37
	3	US- 5,033,812	07-23-1991	Yoshida et al.	385/37
	4	US- 5,436,991	07-25-1995	Sunagawa et al.	385/37
	5	US- 5,200,939	04-06-1993	Nishiwaki et al.	369/4412
	6	US- 5,132,843	07-21-1992	Aoyama et al.	359/573
	7	US- 5,742,433	04-21-1998	Shiono et al.	359/575
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Sheet	2	of	2
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## NON PATENT LITERATURE DOCUMENTS

[illegible]

Examiner Signature	<i>David Peterson</i>	Date Considered	11/16/05
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